

<b>Notice of References Cited</b>	Application/Control No. 09/916,148	Applicant(s)/Patent Under Reexamination BUCKMASTER ET AL.	
	Examiner Russell L. Guill	Art Unit 2123	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mano, M. Morris; "Computer System Architecture", 1982, Prentice-Hall
	V	Habinc, Sandi; Sinander, Peter; "Using VHDL for Board Level Simulation", 1996, Design and Test of Computers, IEEE, Volume 13, Issue 3
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.